

Fig. 3

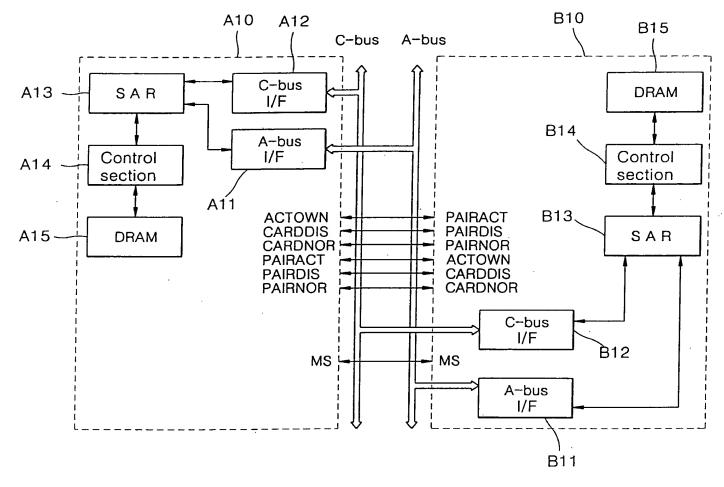


Fig. 4

Header (5byte)			
Node	Slot	Port	
VP			
VP			
. VC] \
VC			
Node	Slot	Port	payload
VP			
VP			
VC			
VC] /
- Connection Related information Band width, cell type etc			